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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/672,148	09/28/2000	Daniel F. Daly	P-9766-US1	7385
	7590 01/21/200 dek Latzer, LLP	EXAMINER		
1500 Broadway 12th Floor		ARMSTRONG, ANGELA A		
New York, NY	10036		ART UNIT	PAPER NUMBER
			2626	
			MAIL DATE	DELIVERY MODE
			01/21/2009	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)				
	09/672,148	DALY ET AL.				
Office Action Summary	Examiner	Art Unit				
	ANGELA A. ARMSTRONG	2626				
The MAILING DATE of this communication app	ears on the cover sheet with the c	orrespondence address				
Period for Reply		0) 00 THETA (00) BAYO				
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period w  - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tim vill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 16 Oc	ctober 2008.					
	action is non-final.					
3) Since this application is in condition for allowar	nce except for formal matters, pro	secution as to the merits is				
closed in accordance with the practice under E	x parte Quayle, 1935 C.D. 11, 45	53 O.G. 213.				
Disposition of Claims						
4)⊠ Claim(s) <u>1-57</u> is/are pending in the application.						
4a) Of the above claim(s) is/are withdraw						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-57</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or	r election requirement.					
Application Papers						
9) The specification is objected to by the Examine	r.					
10) The drawing(s) filed on is/are: a) acce	epted or b) objected to by the B	Examiner.				
Applicant may not request that any objection to the	drawing(s) be held in abeyance. See	e 37 CFR 1.85(a).				
Replacement drawing sheet(s) including the correcti	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).					
11)☐ The oath or declaration is objected to by the Ex	aminer. Note the attached Office	Action or form PTO-152.				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
a) ☐ All b) ☐ Some * c) ☐ None of:						
1. Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau		d				
* See the attached detailed Office action for a list of the certified copies not received.						
Attachment(s)						
Attachment(s)  1) Notice of References Cited (PTO-892)	4) Interview Summary	(PTO-413)				
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Da	nte				
Information Disclosure Statement(s) (PTO/SB/08)     Paper No(s)/Mail Date	5) Notice of Informal P 6) Other:	atent Application				
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## **DETAILED ACTION**

This Office Action is in response to the response filed October 16, 2008.

## **Double Patenting**

1. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. A nonstatutory obviousness-type double patenting rejection is appropriate where the conflicting claims are not identical, but at least one examined application claim is not patentably distinct from the reference claim(s) because the examined application claim is either anticipated by, or would have been obvious over, the reference claim(s). See, e.g., *In re Berg*, 140 F.3d 1428, 46 USPQ2d 1226 (Fed. Cir. 1998); *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and In *re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) or 1.321(d) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent either is shown to be commonly owned with this application, or claims an invention made as a result of activities undertaken within the scope of a joint research agreement.

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

2. Claims 1-57 are rejected on the ground of nonstatutory obviousness-type double patenting as being unpatentable over claims 1-10 of U.S. Patent No. 5,274,738. Although the conflicting claims are not identical, they are not patentably distinct from each other because both the claims to instant application a and the US Patent are directed to digital modular voice processing and although the claims of US Patent 5,274,738 do not specifically teach storing the audio data on a DAT, storing audio data on a DAT was an obvious modification requiring routine skill in the art.

Claim 1 of the application	Claims of US Patent No. 5,274,738

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A modular digital recording logger comprising:	A digital modular voice processing system
	comprising:
A housing;	(Claim 7) The system of claim 1 including a
	housing, said host computer, said first bus, said
	at least one voice processing card, said second
	bus, and said at least one audio card being
	supported by said housing and said at least one
	voice processing card and said at least one
	audio card are physically and electrically
	connected, and said host computer is logically
	connected to said voice processing card and to
	said at least one audio card.
at least two audio circuits in said housing for	an interface for receiving analogue voice
converting analog voice signals to digital voice	signals from a telephone line and converting
signals, each of said audio circuits including at	said analogue voice signals to digital voice
least two terminals for receiving said analog	data,
voice signals, each of said terminals being	
capable of receiving said analog voice signals	
for recording a two-way conversation;	
a circuit in said housing for compressing said	(Claim 5) The system of claim 4 wherein said
digital voice signals received from each of said	signal processor has means for performing
audio circuits to provide compressed voice	voice compression and expansion, automatic

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gain control, dual tone multi-frequency data; extraction and voice activated operations a first bus in said housing for providing (Claim 7) The system of claim 1 including a communication between said audio circuits and housing, said host computer, said first bus, said at least one voice processing card, said second said compressing circuit, said first bus enabling the addition or removal of circuits; bus, and said at least one audio card being supported by said housing and said at least one voice processing card and said at least one audio card are physically and electrically connected, and said host computer is logically connected to said voice processing card and to said at least one audio card. a multiplexer circuit in said housing for c) a voice processing card having at least one digital signal processor and at least one providing communication between said application processor in communication with said at least one digital signal processor, a first compressing circuit and said first bus, wherein interface providing communication between said at least one application processor and said said multiplexer circuit multiplexes voice first bus, and a first time division multiplexer chip in communication with said at least one signals exchanged between said compressing digital signal processor, circuit and said audio circuits on said first bus; d) a second bus in communication with said first time division multiplexer chip, and e) at least one audio card including a second time division multiplexer chip that communicates with said second bus, an audio processor in communication with said second time division multiplexer chip, and a second interface in communication with said audio processor, said second interface having a

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	plurality of ports that provide communication with communication lines.
a host computer for controlling the logger; and	a) a host computer having a host processor,
	and a storage medium, a memory and a bus
	interface in communication with said host
	processor,
a digital audio tape (DAT) drive for storing	(Claim 3) The system of claim 2 wherein said
said compressed voice data.	storage medium is a storage disk.

## Response to Arguments

3. Applicant's arguments with respect to claims 1-57 have been considered but are moot in view of the new ground(s) of rejection.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to ANGELA A. ARMSTRONG whose telephone number is (571)272-7598. The examiner can normally be reached on Monday-Thursday 11:30-8:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Patrick N. Edouard can be reached on 571-272-7603. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <a href="http://pair-direct.uspto.gov">http://pair-direct.uspto.gov</a>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Angela A Armstrong/ Primary Examiner, Art Unit 2626